

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1	13/12/16	Discrete time Signal & System	I	Chalk Talk		
2	14/12/16	Linear Shift Invariant	I	do		
3	15/12/16	Stable and Causal	I	do		
4	16/12/16	Linear Difference Equation	I	do		
5	20/12/16	System down	I	do		
6	21/12/16	do	I	do		
7	26/12/16	Properties of DFT	I	do		
8	27/12/16	Properties of DFT	I	do		
9	27/1/17	Definition of DFT	II	do		
10	31/1/17	Linearity Consideration	II	do		
11	5/1/17	Computation of DFT	II	do		
12	10/1/17	DTT - DFT	II	do		
13	13/1/17	do	II	do		
14	18/1/17	Problem on DFT	II	do		
15	23/1/17	Inverse DFT	II	do		
16	25/1/17	ROC properties	II	do		
17	30/1/17	Inverse z-transform	II	do		
18	31/1/17	Problem on z-transform	II	do		
19	4/2/17	Relationship B/L O DFT & z	II	do		

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20	2/2/12	Fire Detector	<u>I</u>	Chalk Talk		
21	3/2/12	Structure of FIR	<u>I</u>	do		
22	5/2/12	- do -	<u>I</u>	do		
23	7/2/12	- do -	<u>I</u>	do		
24	8/2/12	Better world FIR	<u>I</u>	do		
25	9/2/12	- do -	<u>I</u>	do		
26	11/2/12	Chaby Mev FIR	<u>I</u>	do		
27	20/2/12	- do -	<u>I</u>	do		
28	24/2/12	S-t Camera	<u>I</u>	do		
29	24/2/12	- do -	<u>I</u>	do		
	23/2/12					
30	23/2/12	FIR Structure	<u>I</u>	do		
31	29/2/12	- do -	<u>I</u>	do		
32	28/2/12	Leindas band FIR	<u>I</u>	do		
33	6/3/12	Problem on FIR	<u>I</u>	do		
34	9/3/12	- do -	<u>I</u>	do		
35	10/3/12	Sampling technique	<u>I</u>	do		
36	14/3/12	- do -	<u>I</u>	do		
37	12/3/12	Interpolation	<u>I</u>	do		
38	14/3/12	Decimation	<u>I</u>	do		

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39	15/3/17	Sample Rate Conversion	<u>TV</u>	Chalk Talk		
	16/3/17					
40	16/3/17	DSP processor Data path	<u>V</u>	PPT		
41	20/3/17	MAC	<u>V</u>	PPT		
42	21/3/17	VLSI Pipeline	<u>V</u>	PPT		
43	22/3/17	TMS 3205X	<u>V</u>	PPT		
44	23/3/17	Register operation	<u>V</u>	PPT		
45	28/3/17	do	<u>V</u>	PPT		
46	30/3/17	Memory operation	<u>V</u>	PPT		
47	3/4/17	do	<u>V</u>	PPT		
48	4/4/17	Flag Register	<u>V</u>	PPT		
49	6/4/17	on-chip pipeline	<u>V</u>	PPT		